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LYSON
Patent 7/10/03

Atty Docket No. 4860.P2438

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Biyabani, Sara R.

Serial No. 09/589,621

Filed: June 7, 2000

For: DECOUPLING A COLOR
BUFFER FROM MAIN
MEMORY

) Examiner: Caschera, Antonio A.

) Art Unit: 2697

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Technology Center 2600

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

In the Claims

1. (Currently Amended) A unified memory architecture that decouples a color buffer from a main memory in a computer, the architecture comprising:

a sole memory controller connected to the main memory to manage use of the main memory between a graphics subsystem and a processing unit, the memory controller operable for partitioning an address space for the color buffer in main memory into two logical buffers, operable for designating one logical buffer as a frame-preparation memory and one logical buffer as a refresh memory, operable for connecting the frame-preparation memory to the graphics subsystem and operable for connecting the refresh memory to a display device, wherein color data is written into the frame-preparation

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